

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L10	1	1987-356745.NRAN.	DERWENT	OR	OFF	2005/05/18 15:59
L11	1393	tile\$1 and array and schedul\$3	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L12	535	tile\$1 and (two-dimension\$2 or (two adj dimension\$2) near5 array) and schedul\$3	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L14	3835	(unisys).as.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/18 15:59
L15	1570	(unisys adj corporation).as.	USPAT	OR	OFF	2005/05/18 15:59
L16	0	((unisys adj corporation).as.) and ("a special purpose processor for off-loading many operating system functions in a large data processing system").ti.	USPAT	OR	OFF	2005/05/18 15:59
L17	1	((unisys adj corporation).as.) and (special adj purpose adj processor adj10 large adj data adj processing).ti.	USPAT	OR	OFF	2005/05/18 15:59
L29	288	butler same circuit	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L30	365	butler same circuit\$1	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L31	6	(butler same circuit\$1) and tile\$1	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L32	335	(butler same circuit\$1) not butler. in.	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L33	23	((butler same circuit\$1) not butler.in.) and array) and (multiprocessor\$1 or multi-processor\$1 or processors)	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L36	14	(intergrated adj circuit\$1) and ((two-dimensional or 2-D) adj array)	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L37	0	(intergrated adj circuit\$1) and ((two-dimensional or 2-D) adj array) and scheduler\$1	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L38	2	(intergrated adj circuit\$1) and (array) and scheduler\$1	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59

L39	0	(intergrated adj circuit\$1) and (logic adj gate\$1) and scheduler\$1	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L40	0	(intergrated adj circuit\$1) and (logical adj gate\$1) and scheduler\$1	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L41	1	(intergrated adj circuit\$1) and (gate\$1) and scheduler\$1	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L42	1	tile\$1 and (two-dimension\$2 or (two adj dimension\$2) near5 array) and schedul\$3 and intergrated	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L43	242	(integrated adj circuit\$1) and (logic adj gate\$1) and scheduler\$1	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L44	39	(integrated adj circuit\$1) and ((two-dimensional or 2-D) adj array) and scheduler\$1	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L46	175	((butler same circuit\$1) not butler.in.) and array	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L47	1	((butler same circuit\$1) not butler.in.) and (intergrated adj circuit\$1)	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L48	0	((butler same chip\$1) not butler.in.) and (intergrated adj circuit\$1)	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L49	8	((butler) not butler.in.) and (intergrated adj circuit\$1)	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L50	258	(logic same gate\$1) and (intergrated adj circuit\$1)	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L51	28	((logic same gate\$1) and (intergrated adj circuit\$1)) and array and (multiprocessor\$1 or multi-processor\$1 or processors)	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L52	1	((logic same gate\$1) and (intergrated adj circuit\$1)) and array and (multiprocessor\$1 or multi-processor\$1 or processors)) and scheduler\$1	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L53	3	((logic same gate\$1) and (intergrated adj circuit\$1)) and array and (multiprocessor\$1 or multi-processor\$1 or processors)) and schedul\$3	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59

L54	10	(intergrated adj circuit) and ((two-dimensional or 2-D) adj array)	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L55	232	butler with (circuit\$1 chip\$1)	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L56	243	butler with (circuit\$1 chip\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/18 15:59
L57	13	(butler with (circuit\$1 chip\$1) same (integrated near circuit\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/18 15:59
L58	4	(butler with (circuit\$1 chip\$1) same (integrated near circuit\$1)) and tile\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/18 15:59
L62	0	(butler with (circuit\$1 chip\$1) same (integrated near circuit\$1)) and stim-wait	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/18 15:59
L63	1	stim-wait	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/18 15:59
L73	27	(campbell near eric).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/18 15:59
L74	6	(hugo near simpson).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/18 15:59
L75	1	(pairs near5 (control\$4 adj variables)) and (integrated near circuit\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/18 15:59
L76	0	(pairs near5 (control\$4 adj variables)) and (schedul\$3 near3 task\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/18 15:59
L77	0	(pairs near5 (control\$4 adj variables)) and (processor\$1 near10 task\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/18 15:59
L78	58	(pair\$1 near5 variables) and (processor\$1 near10 task\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/18 15:59

L79	19	(pair\$1 near5 variables) and (schedul\$4 near5 task\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/18 15:59
L80	686	((two-dimension\$2 near2 array) and circuit\$1) and schedul\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/18 15:59
L81	0	((two-dimension\$2 near2 array) and circuit\$1) same (schedul\$4 near task\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/18 15:59
L82	7	((two-dimension\$2 near2 array) and circuit\$1) same schedul\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/18 15:59
L83	26	(butler with matrix\$2) and schedul\$4	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 15:59
L84	12	((two-dimension\$2 near2 array) with circuit\$1) and (schedul\$4 near10 task\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/05/18 15:59
L91	1082	(718/103,105).ccls.	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 16:25
L92	200	((718/103,105).ccls. ) and (round-robin or (round adj robin))	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 16:25
L93	103	((718/103,105).ccls. ) and (round-robin or (round adj robin))) and (schedul\$3)	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 16:25
L94	0	((718/103,105).ccls. ) and (round-robin or (round adj robin))) and (schedul\$3)) AND cmos	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 16:26
L95	3	((718/103,105).ccls. ) and (round-robin or (round adj robin))) AND cmos	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 16:26
L96	3	((718/103,105).ccls. ) and (round-robin or (round adj robin))) AND (complementary adj metal adj oxide adj semiconductor)	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 16:26
L97	4	((718/103,105).ccls. ) AND (complementary adj metal adj oxide adj semiconductor)	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 16:26
L98	0	((718/103,105).ccls. ) and (round-robin or (round adj robin))) and (schedul\$3)) AND (oxide adj semiconductor)	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 16:27

L99	6	(((((718/103,105).ccls. ) and (round-robin or (round adj robin))) and (schedul\$3)) AND (semiconductor)	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 16:27
L100	13	((718/103,105).ccls.) and tile\$1	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 16:27
L101	285	((718/103,105).ccls.) and array	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 16:27
L102	51	((718/103,105).ccls.) and (two-dimension\$2 or (two adj dimension\$2) near5 array)	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 16:27

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L104	107	103 and (round-robin or (round adj robin))	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 16:31
L105	29	103 and (round-robin or (round adj robin)) and (schedul\$3)	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 16:32
L107	2	103 and (round-robin or (round adj robin)) AND cmos	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 16:32
L108	9	103 AND (complementary adj metal adj oxide adj semiconductor)	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 16:33
L110	4	103 and (round-robin or (round adj robin)) and (schedul\$3) AND (semiconductor)	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 16:34
L111	6	103 and tile\$1	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 16:34
L112	184	103 and array	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 16:34
L113	17	103 and (two-dimension\$2 or (two adj dimension\$2) near5 array)	US-PGPUB; USPAT; DERWENT	OR	OFF	2005/05/18 16:34